

LCD Type1 MCU

Item	Type	VDD	ROM	RAM	System Clock	I/O	I/P	O/P	LCD Seg*Com, duty, bias	Timer/ Counter 8-bit	Time base	Inter-rupt Ext.	Inter-rupt Int	PFD	WDT	Stack	CPU core	PKG	Special function
TTU261L	mask	1.2V -- 1.8V	1K*16	48*4	32KHz/ 128KHz (RC) 32KHz (X'tal/RC)	4	4	2	13*3,12*4 Bias:1/2	1	V	2	2	V	V	2	4-bit CPU ET416	dice	
TTU262L	mask	1.2V--1.8V	4K*16	256*4	16KHz--128KHz(RC) 32KHz(X'tal/RC)	16	--	--	37*3/36*4/35*5/34*6;Bias:1/2,1/3	2 (or 16bit*1)	V	1	3	V	V	4	4-bit CPU ET416	dice	8 I/O shared with seg drivers
TTR262*	OTP	2.2V--5.5V	4K*16	256*4	16KHz--128KHz(RC) 32KHz(X'tal/RC)	16	--	--	37*3/36*4/35*5/ 34*6;Bias:1/2,1/3	2 (or 16bit *1)	v	1	3	v	v	4	4-bit CPU ET416	dice	8 I/O shared with seg drivers
TTU262*	mask	2.2V--5.5V	4K*16	256*4	16KHz--128KHz(RC) 32KHz(X'tal/RC)	16	--	--	37*3/36*4/35*5/34*6;Bias:1/2,1/3	2 (or 16bit *1)	v	1	3	v	v	4	4-bit CPU ET416	dice	8 I/O shared with seg drivers

LCD Type2 MCU

Item	Type	VDD	ROM	RAM	System Clock	I/O	LCD Seg*Com, duty, bias	Timer/ Counter 8-bit	Inter-rupt Ext.	Inter-rupt Int	PFD	WDT	RTC	CPU core	PKG	Special function
TTU251	mask	2.2V -- 5.5V	4K*8	128*8	400KHz – 4MHz (X'tal/RC) 32KHz (X'tal)	16	32 * 4, 1/4, 1/3 33 * 3, 1/3, 1/3	2 (or 16bit *1)	2	3	V	V	V	8-bit CPU 65C02 std	48 SSOP 100 QFP	1.LCD mask option R type or C type 2.Power saving halt mode
TTR251	OTP	2.2V -- 5.5V	4K*8	128*8	400KHz--4MHz (X'tal/RC) 32KHz (X'tal)	16	32 * 4, 1/4, 1/3 33 * 3, 1/3, 1/3	2 (or 16bit *1)	2	3	V	V	V	8-bit CPU 65C02 std	48 SSOP 100 QFP	1.LCD mask option R type or C type 2.Power saving halt mode

A_D&LCD Type MCU

Item	Type	VDD	ROM	RAM	System Clock	I/O	I/P	O/P	A/D Bit CH	LCD Seg*Com, duty, bias	Timer/ Counter 8-bit	Time base	Inter-rupt Ext.	Inter-rupt Int	LVR	CPU core	PKG	Special function
TTU212	MASK	2.0V--5.5V	8K*8	128*8	1MHz--4MHz(Resonator) 400KHz--4MHz(RC)	14	8	24	8 4	16*4, 1/4 1/3 16*3, 1/3 1/3	2 (or 16bit*1)	v	2	4	v Adj	8-bit CPU 65C02 std	Dice/64 LQFP	Power saving Stop mode
TTR212	OTP	2.4V -- 5.5V	8K*8	128*8	1MHz--4MHz(Resonator) 400KHz -- 4MHz (RC)	14	8	24	8 4	16 * 4, 1/4 1/3 16 * 3, 1/3 1/3	2 (or 16bit *1)	V	2	4	v Adj	8-bit CPU 65C02 std	64 LQFP	1.Power saving stop mode *TTR212 OTP can support TTU212